



Applicant: Seiki Aguro

Serial No.: 09/281,042

Filed: 03/30/1999

Title: COMPUTER SYSTEM

Docket No. TIJ-26495

Art Unit: 2123

Examiner: Jones, H.

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RESPONSE TO FINAL REJECTION

Assistant Commissioner For Patents
Box AF
Washington, DC 20231

Dear Sir:

MAILING CERTIFICATE UNDER 37 C.F.R. § 1.8(a)

I hereby certify that on this date, the above correspondence is being deposited with the US Postal Service as First Class Mail in an envelope addressed to: Assistant Commissioner for Patents, Box AF, Washington, DC 20231.

William B. Kempler
William B. Kempler, Reg. No. 28,228

10/11/01
Date

In response to the Official Action of June 19, 2001, please amend the above-identified application as follows:

In the Claims:

Please amend claim 4 and 8 as follows:

4. (Amended) An integrated circuit computer system comprising:
a processor interconnected with memory and peripheral circuits on said integrated circuit;
a scan-path interface circuit for reading out contents of a predetermined memory or register in said system;